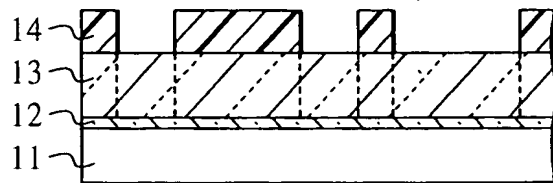




FIG. 1A EFFECTIVE WAFER AREA



NONEFFECTIVE WAFER AREA
 (PERIPHERAL WAFER AREA)

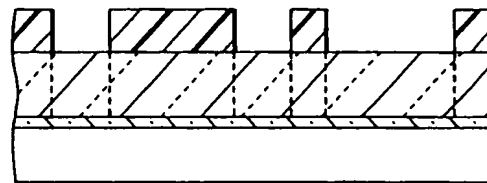


FIG. 1B

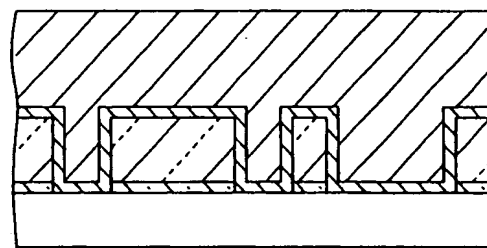
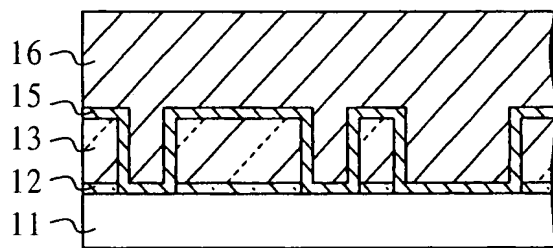


FIG. 1C

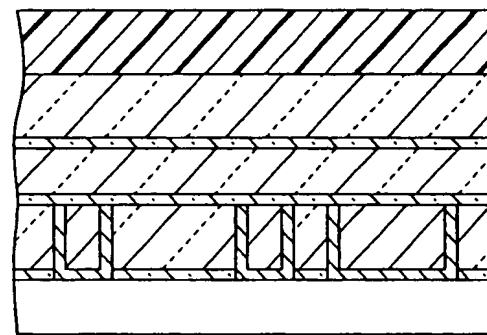
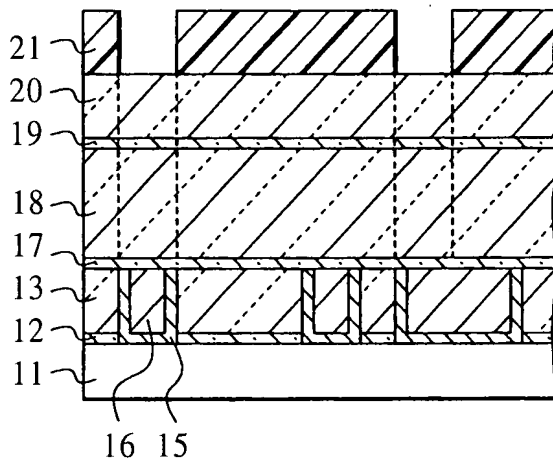


FIG. 1D

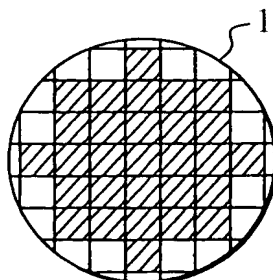
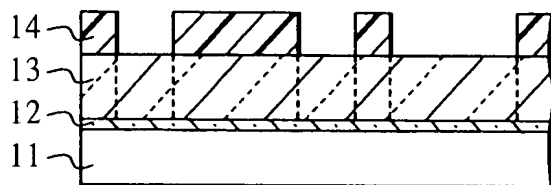


FIG. 12A EFFECTIVE WAFER AREA
 PRIOR ART



NONEFFECTIVE WAFER AREA
 (PERIPHERAL WAFER AREA)

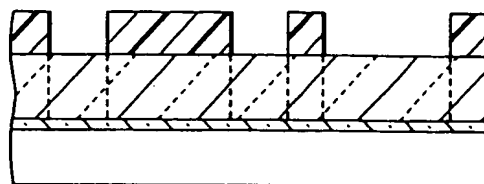


FIG. 12B
 PRIOR ART

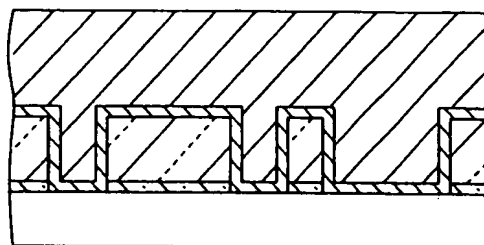
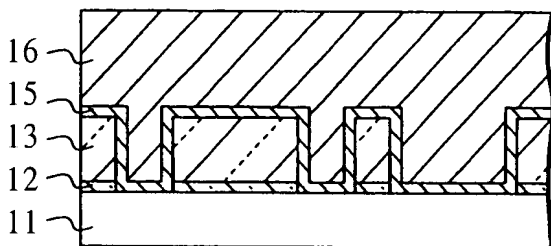


FIG. 12C
 PRIOR ART

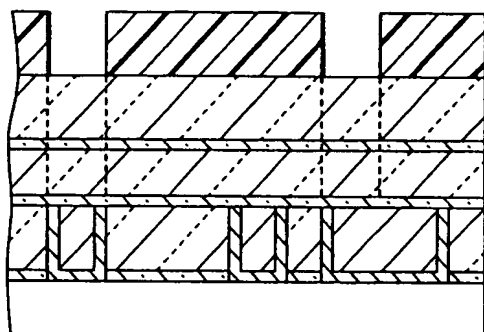
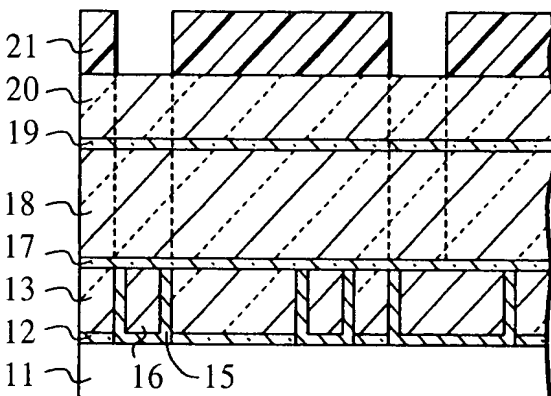


FIG. 12D
 PRIOR ART

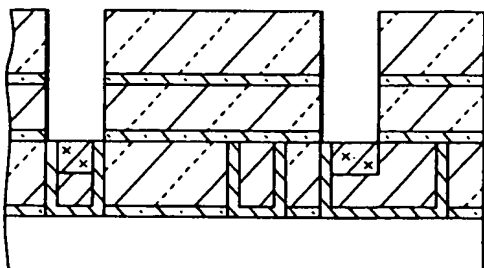
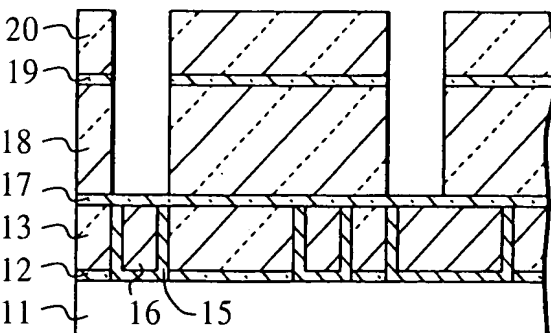


FIG. 13A PRIOR ART

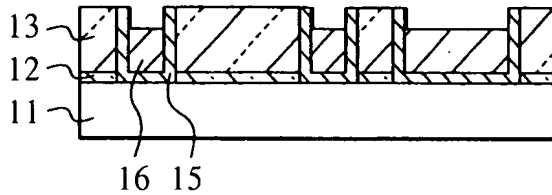


FIG. 13B PRIOR ART

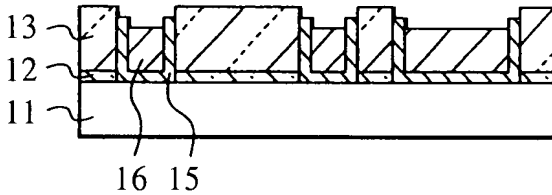


FIG. 13C PRIOR ART

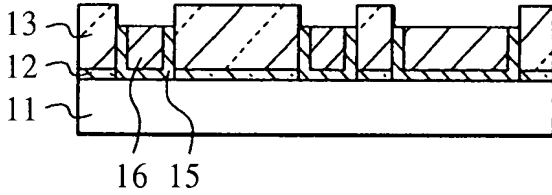


FIG. 13D PRIOR ART

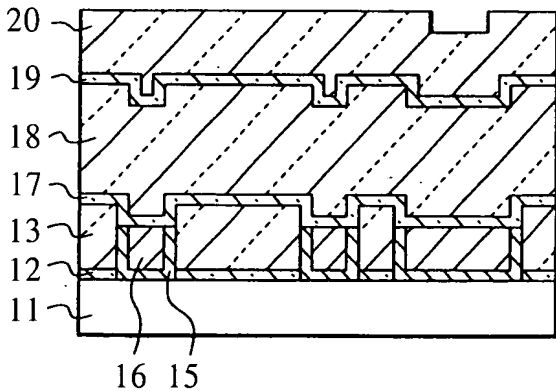


FIG. 13E PRIOR ART

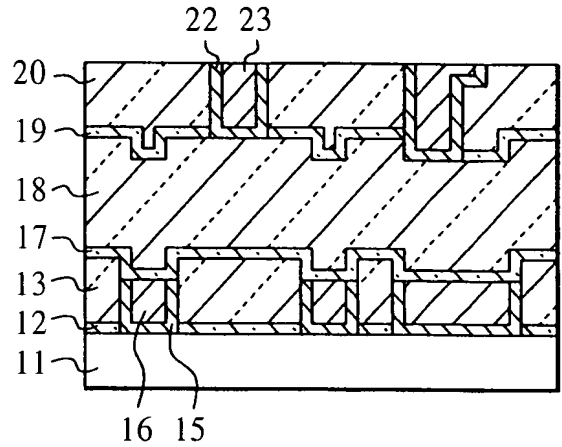


FIG. 13F PRIOR ART

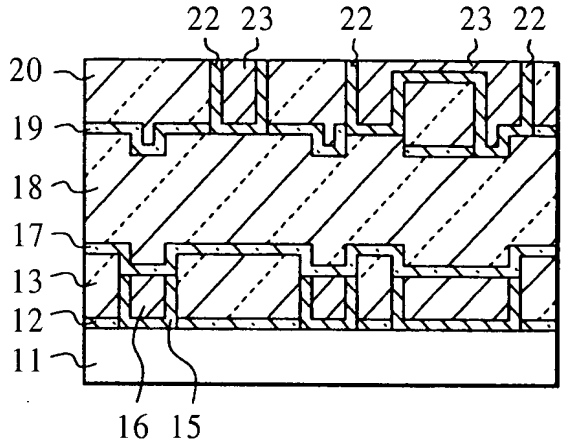


FIG. 13G PRIOR ART

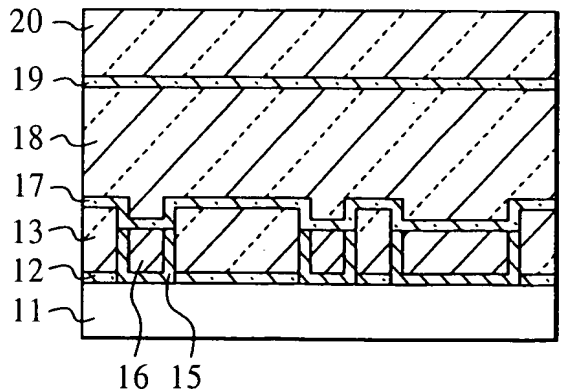


FIG. 14A
PRIOR ART

EFFECTIVE WAFER AREA

NONEFFECTIVE WAFER AREA
(PERIPHERAL WAFER AREA)

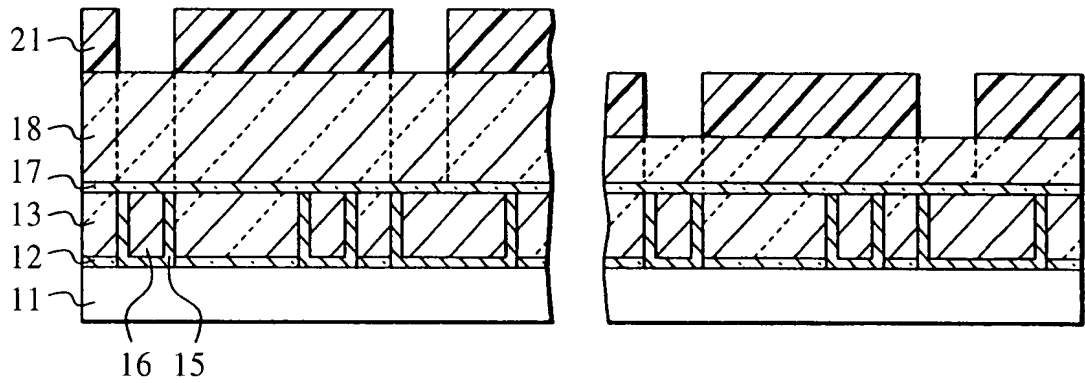


FIG. 14B
PRIOR ART

